

Extending Buffer-Aware Worst-Case Timing Analysis of Wormhole NoCs

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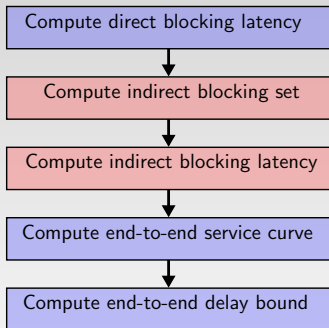
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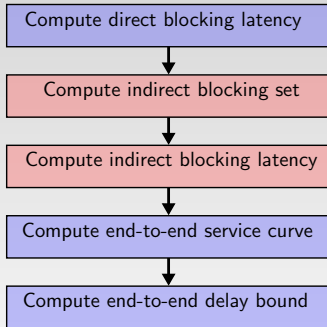
Objective

Extend our previously published approach (RTAS18) to cover bursty traffic flows

RTAS18: Main steps

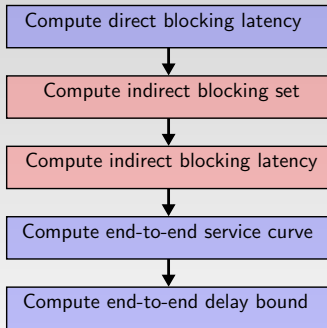


RTAS18: Main steps



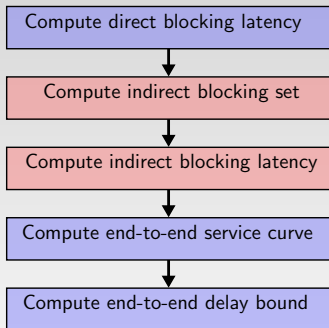
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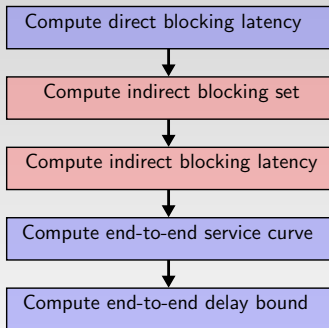
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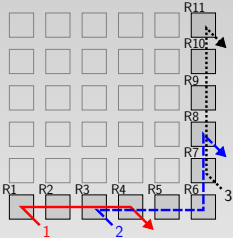
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- ▶ Derive corresponding **Indirect Blocking Latency**

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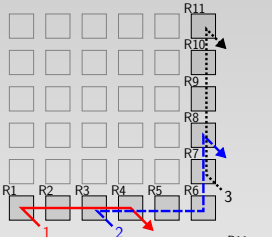


- ▶ Account for **Direct Blocking Latency**
- ▶ Analyze potential indirect blocking patterns
- ▶ Derive corresponding **Indirect Blocking Latency**
- ▶ Compute service curve and **worst-case latency bound**

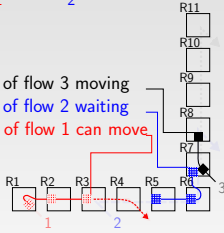
RTAS18: Limitations



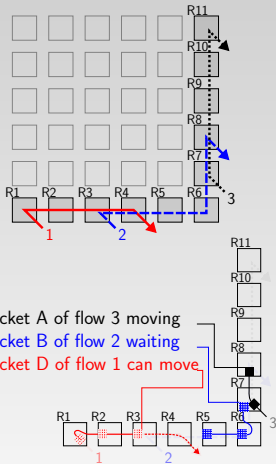
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- packet A of flow 3 moving
- packet B of flow 2 waiting
- packet D of flow 1 can move

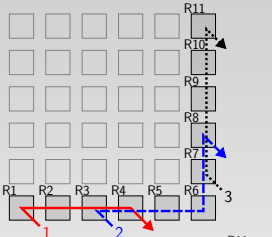


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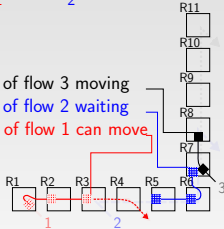


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 $IB_1 = \{ \}$

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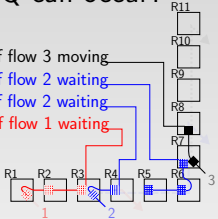


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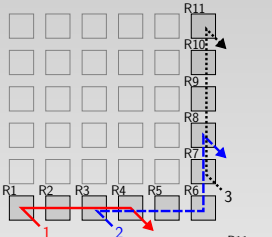
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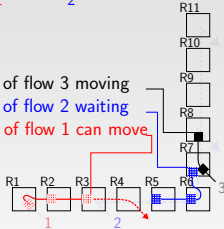


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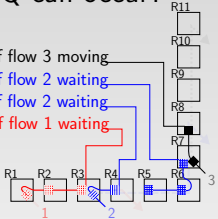


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CPQ causes unexpected indirect blocking scenarios

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Leads and Perspectives

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- ▶ **Account for CPQ** in indirect blocking pattern analysis: How?
- ▶ Account for specificities of IB analysis covering CPQ during service curve computation
- ▶ We have a poster! Come and check it out 😊