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Time4Sys - Integrating Temporal Performance Verification in your Engineering Practices

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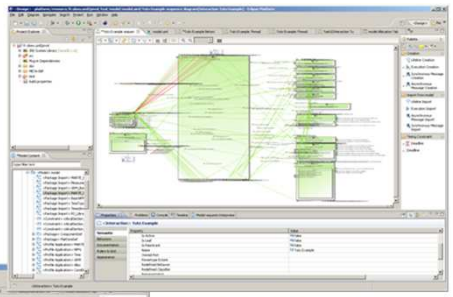
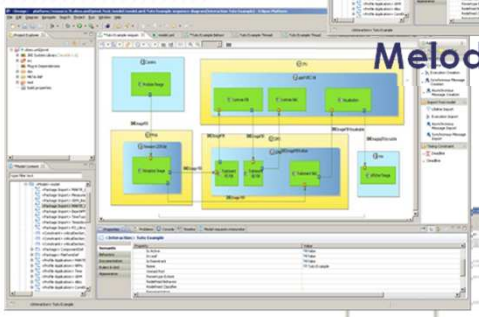
How can industry Benefit from existing scheduling verification techniques/tools ?

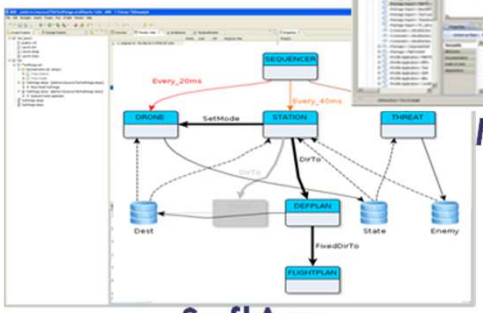
Various design tools



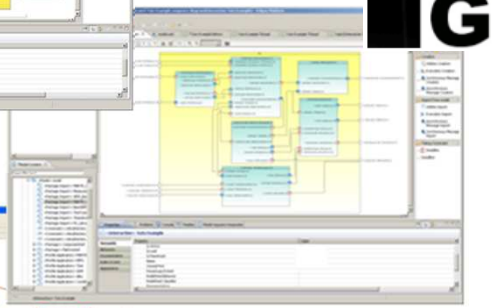
MelodyCCM Spatial



Capella



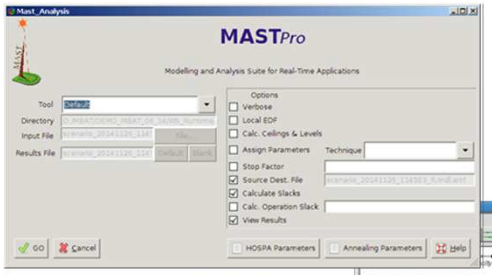
SoftArc



MelodyCCM

GAP

Various timing verification tools



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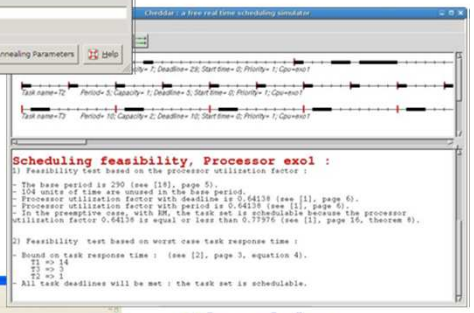
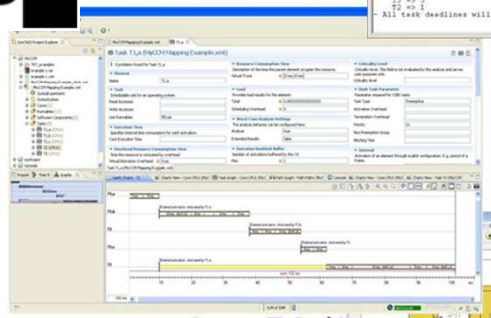
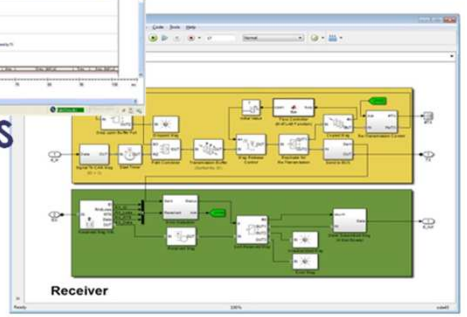


Cheddar



SymTA/S



SimEvents

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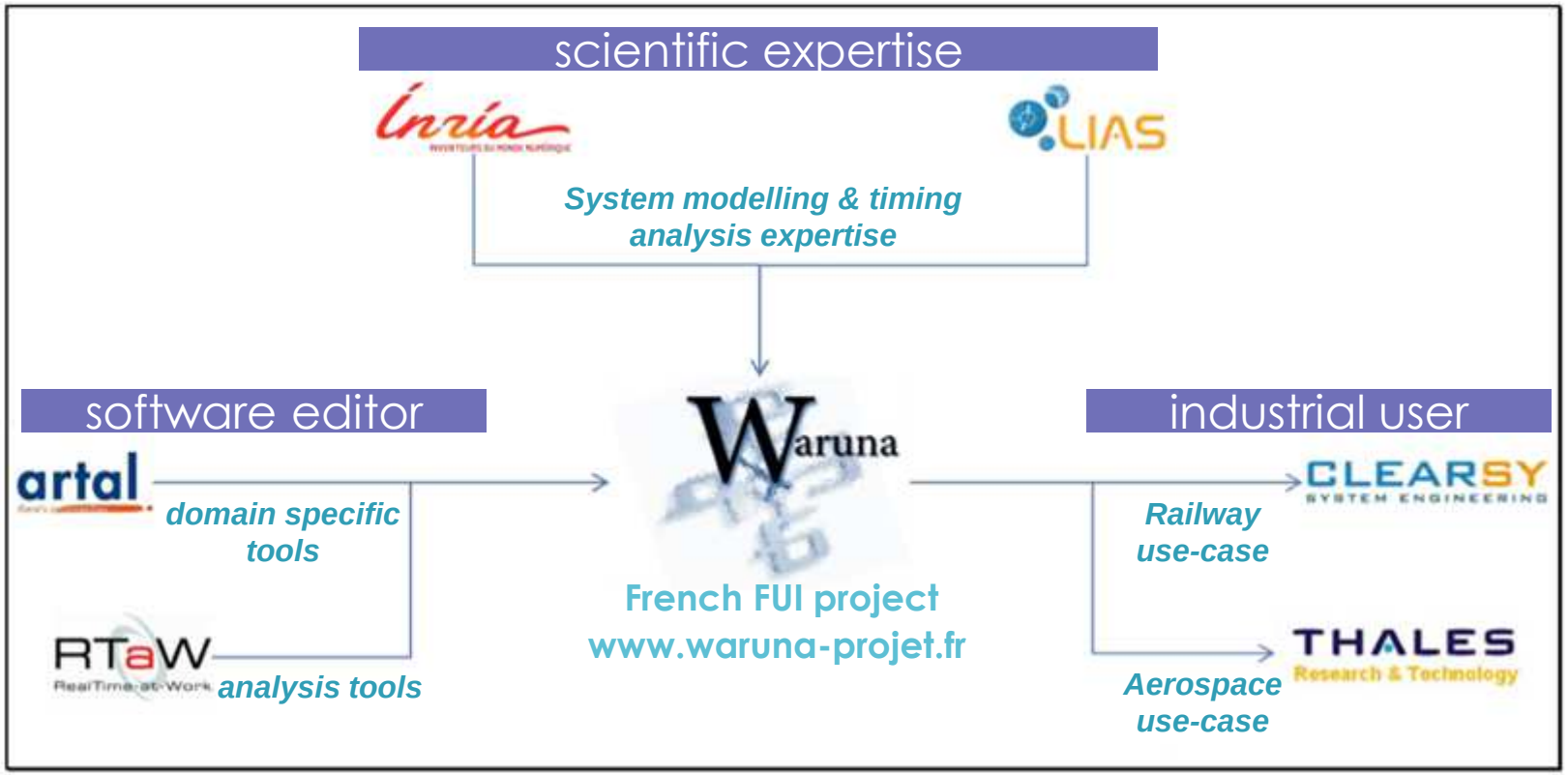
WARUNA project objectives

O1: Develop a modelling framework allowing to perform model-based scheduling verification of real-time systems at early design stages

O2: Reach TRL6 at the end of the project by validating the developed framework on representative industrial use-cases



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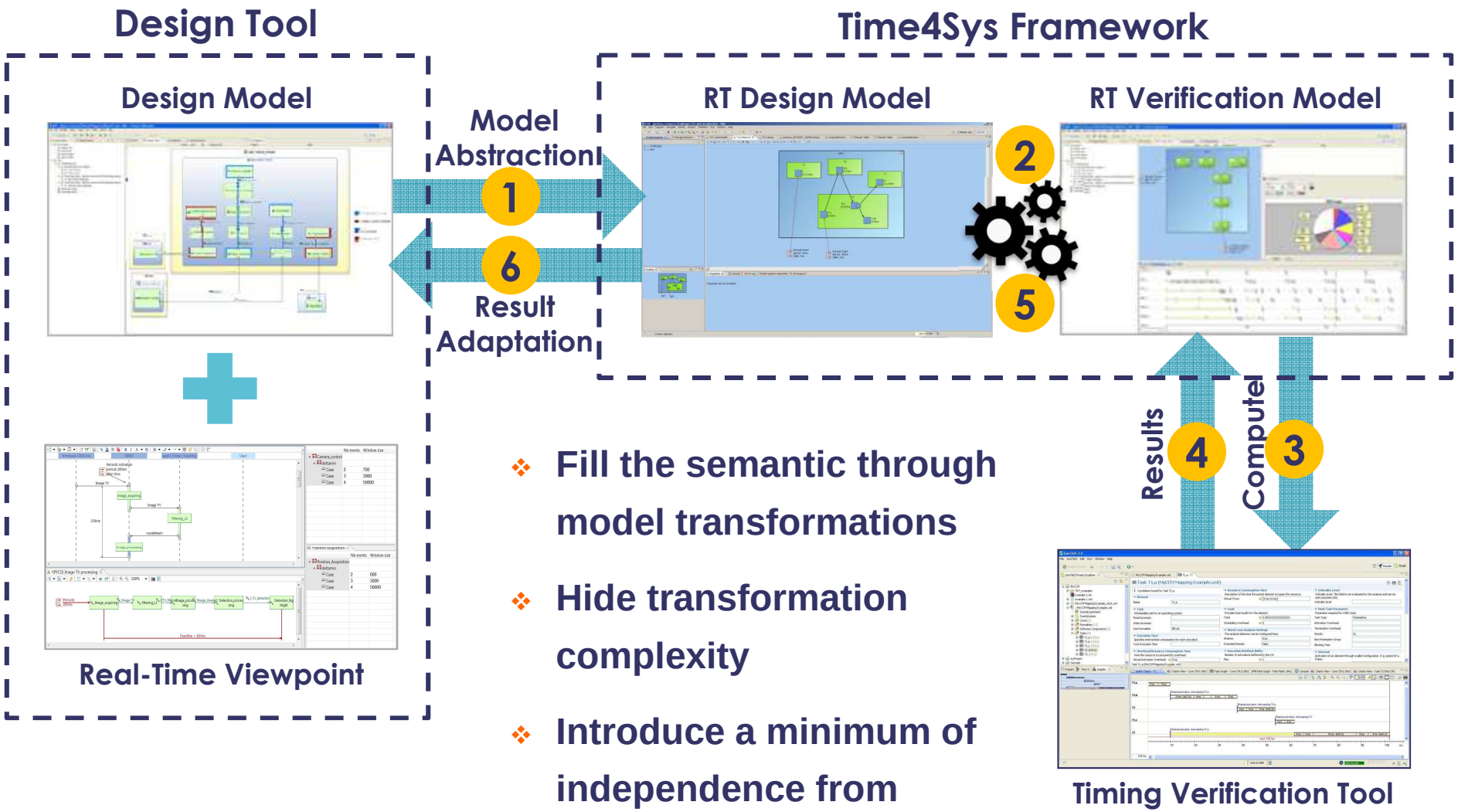
Connecting design activities to timing performance verification via Time4Sys



- Fill the semantic through model transformations
- Hide transformation complexity
- Introduce a minimum of independence from timing verification tools specificities

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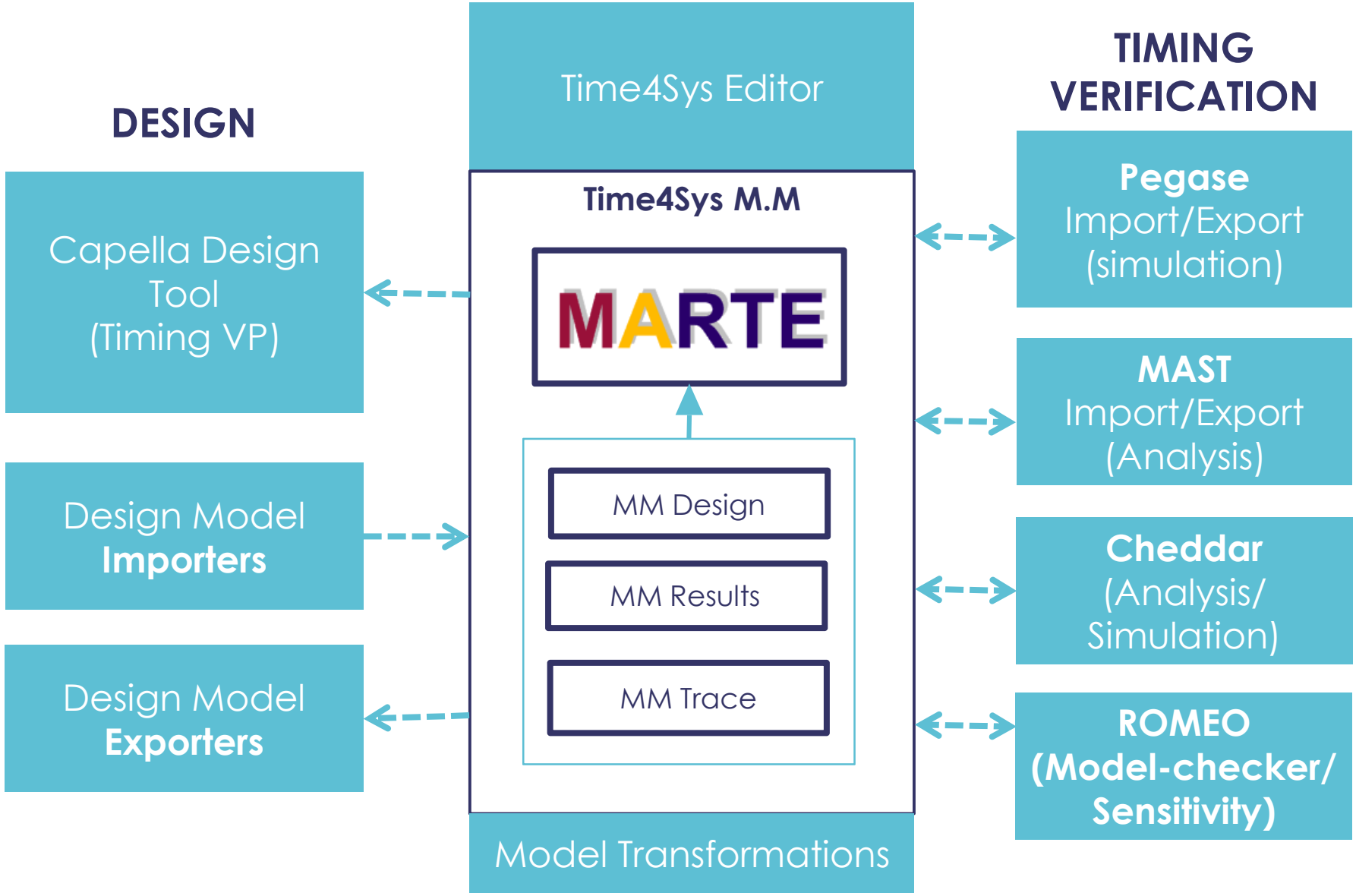
Time4Sys Architecture



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